AMENDMENTS TO THE CLAIMS

This listing of claims replaces all prior versions, and listings, of claims in the application.

1. (Currently Amended) A printed circuit board comprising:

external surfaces and a plurality of internal-layer signal lines traced within an internal layer of the printed circuit board between the external surfaces; and

a plurality of outermost-layer signal lines traced on ~~at least~~ one of the external surfaces of the printed circuit board and connected with respective internal-layer signal lines ~~via~~ through via holes, wherein the outermost-layer signal lines are each connected with respective connector lines of a connector mounted on one of the external surfaces of the printed circuit board, and ~~the~~total lengths of respective outermost-layer signal lines ~~have different lengths from each other based on the lengths of the connector lines~~and connector lines of the connector connecting corresponding outermost-layer signal lines to respective connected via holes are substantially equal.

2. (Previously Presented) The printed circuit board according to Claim 1, wherein the outermost-layer signal lines are each straight and are free of bends.

Claim 3 (Cancelled).